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PATENT APPLICATION
Q77404

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re application of

Keiji HAYASHIDA, *et al.*

Appln. No.: 10/664,884

Group Art Unit: 2817

Confirmation No.: 4264

Examiner: Arnold M. Kinhead

Filed: September 22, 2003

For: PHASE-LOCKED LOOP CIRCUIT REDUCING STEADY STATE PHASE ERROR

SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. §1.111

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Supplemental to the Amendment filed June 8, 2005, and further in response to the Office Action dated March 8, 2005, please further amend the above-identified application as follows on the accompanying pages.

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